

<b>Notice of References Cited</b>	Application/Control No. 10/672,967	Applicant(s)/Patent Under Reexamination CALYANAKOTI ET AL.	
	Examiner Mary J. Steelman	Art Unit 2191	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,274,811 A	12-1993	Borg et al.	717/128
*	B	US-6,009,270 A	12-1999	Mann, Daniel	717/128
*	C	US-6,173,395 B1	01-2001	Wisor et al.	712/236
*	D	US-6,247,170 B1	06-2001	Giroux, Michael	717/131
*	E	US-6,249,906 B1	06-2001	Levine et al.	717/153
*	F	US-6,263,488 B1	07-2001	Fortin et al.	717/127
*	G	US-6,467,083 B1	10-2002	Yamashita, Fumiaki	717/128
*	H	US-6,681,321 B1	01-2004	Dale et al.	712/227
*	I	US-6,968,546 B2	11-2005	Lueh, Guei-Yuan	717/158
	J	US-			
	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP2000076094 A	03-2000	JP	RICOH	GO6F 11/28
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Agarwa, Anant; Sites, Richard L; Horowitz, Mark; "ATUM: A New Technique for Capturing Address Traces Using Microcode", p. 119-127, 1986 IEEE retrieved 7/11/2006.
	V	Ball, Thomas; Laurs, James R.; "Optimally Profiling and Tracing Programs", p. 1319-1360, 1994 ACM, retrieved 7/11/2006.
	W	Chen, Bing; Ali, Musawir; "Trace Cache", p. 1-6, <URL www.cs.ucf.edu/~mali/TraceCache.doc>, retrieved 7/11/2006 from www.google.com search.
	X	Oliver, Richard L; Teller, Patricia J; "Generating Dynamically Scheduled Memory Address Traces", p. 245-250, 1998 IEEE, retrieved 7/11/2006.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.